

APPLICATION DATA SHEET (37 CFR 1.76)

APPLICATION INFORMATION

Title Line One:: Margin Test Methods and Circuits
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DOMESTIC PRIORITY CLAIM

This application claims the benefit under 35 U.S.C. §120 of U.S. Patent Application Serial No. 10/441,461, entitled "Methods and Circuits for Performing Margining Tests in the Presence of a Decision Feedback Equalizer" by Fred F. Chen, filed May 20, 2003.

CONTINUITY INFORMATION

This application is a continuation in part of U.S. Patent Application Serial No. 10/441,461, entitled "Methods and Circuits for Performing Margining Tests in the Presence of a Decision Feedback Equalizer" by Fred F. Chen, filed May 20, 2003.

FOREIGN PRIORITY INFORMATION

None